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MODULATION POWER SUPPLY
CIRCUITS
First Named Inventor: Mehmet Nalbant
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Submitted by:	Elec. Sign.	Sign. Capacity
Roger W Blakely Jr. Registered Number: 25831	Roger W Blakely Jr	Attorney
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Comments		



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
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<h3>US Patent Documents</h3> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td></td><td>1</td><td>4870555</td><td>1989-09-26</td><td>White</td><td></td><td></td><td></td></tr><tr><td></td><td>2</td><td>4953068</td><td>1990-08-28</td><td>Henze</td><td></td><td></td><td></td></tr><tr><td></td><td>3</td><td>5519599</td><td>1996-05-21</td><td>Shinada et al</td><td></td><td></td><td></td></tr><tr><td></td><td>4</td><td>5631810</td><td>1997-05-20</td><td>Takano</td><td></td><td></td><td></td></tr><tr><td></td><td>5</td><td>5726869</td><td>1998-03-10</td><td>Yamashita et al</td><td></td><td></td><td></td></tr><tr><td></td><td>6</td><td>5734563</td><td>1998-03-31</td><td>Shinada</td><td></td><td></td><td></td></tr><tr><td></td><td>7</td><td>5907481</td><td>1999-05-25</td><td>Svardsjo</td><td></td><td></td><td></td></tr><tr><td></td><td>8</td><td>5999420</td><td>1999-12-07</td><td>Aonuma et al</td><td></td><td></td><td></td></tr><tr><td></td><td>9</td><td>6072701</td><td>2000-06-06</td><td>Sato et al</td><td></td><td></td><td></td></tr><tr><td></td><td>10</td><td>6078509</td><td>2000-06-20</td><td>Jacobs et al</td><td></td><td></td><td></td></tr><tr><td></td><td>11</td><td>6101104</td><td>2000-08-08</td><td>Eng</td><td></td><td></td><td></td></tr><tr><td></td><td>12</td><td>6111769</td><td>2000-08-29</td><td>Zhang et al</td><td></td><td></td><td></td></tr><tr><td></td><td>13</td><td>6115276</td><td>2000-09-05</td><td>Mao</td><td></td><td></td><td></td></tr></tbody></table>						init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass		1	4870555	1989-09-26	White					2	4953068	1990-08-28	Henze					3	5519599	1996-05-21	Shinada et al					4	5631810	1997-05-20	Takano					5	5726869	1998-03-10	Yamashita et al					6	5734563	1998-03-31	Shinada					7	5907481	1999-05-25	Svardsjo					8	5999420	1999-12-07	Aonuma et al					9	6072701	2000-06-06	Sato et al					10	6078509	2000-06-20	Jacobs et al					11	6101104	2000-08-08	Eng					12	6111769	2000-08-29	Zhang et al					13	6115276	2000-09-05	Mao			
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Remarks

Note: Remarks are not for responding to an office action.



Applicant, in accordance with his duty of disclosure under 37 CFR 1.56 and in accordance with 37 CFR 1.97(b)(3), hereby submits this Electronic Information Disclosure Statement citing U.S. Patent documents for consideration by the Examiner. Pursuant to 37 CFR 1.97, the submission of this Electronic Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability. This Electronic Information Disclosure Statement is being filed prior to substantive examination of the claims. Pursuant to 37 CFR 1.97(b), no fee should be required for the filing of this Electronic Information Disclosure Statement. In the event it is determined that a fee is due, please charge the fee to Deposit Account No. 02-2666. Applicant respectfully requests that the cited documents be considered and that the form be initialed by the Examiner to indicate such consideration and a copy thereof returned to the Applicant's attorney of record. An IDS citing other art is being transmitted by U.S. Mail on this date.

Signature

Examiner Name	Date